

CLAIM CHANGES:

1. Cancelled
2. A semiconductor device, comprising:
 - a semiconductor substrate;
 - a pair of first diffusion layers formed within said semiconductor substrate;
 - a gate insulating film including a first insulating film portion formed on that portion of said semiconductor substrate which is positioned between said first diffusion layers and a second insulating film portion positioned on both edges of said first insulating film portion, a thickness of said second insulating film portion being larger than a thickness of said first insulating film portion;
 - a gate electrode formed on said gate insulating film;
 - a gate side wall insulating film formed on a side surface of said gate electrode and on a side surface of said second insulating film portion; and
 - a second diffusion layer formed apart from said first diffusion layers within that portion of said semiconductor substrate which is positioned below said first insulating film portion.
3. Cancelled
4. Cancelled
5. The semiconductor device according to claim 2, wherein said first diffusion layers further comprises:
 - a pair of extension regions formed below said gate side wall insulating film apart from said second diffusion layer; and
 - a pair of source-drain regions formed in contact with said extension regions on a side opposite said second diffusion layer.
6. The semiconductor device according to claim 2, wherein said gate side wall insulating film comprises:
 - a third side wall portion formed on the side surface of said gate electrode and on the side surface of said second insulating film portion; and
 - a fourth side wall portion formed on a side surface of said third side wall portion.
7. Cancelled
8. The semiconductor device according to claim 2, further comprising an interlayer insulating film formed to surround said gate side wall insulating

New Divisional Application of MATSUDA et al.

film, an upper surface of said interlayer insulating film being substantially equal to an upper surface of said gate electrode.

9. Cancelled
10. The semiconductor device according to claim 2, wherein a conductivity type of said second diffusion layer is opposite the conductivity type of said semiconductor substrate.
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